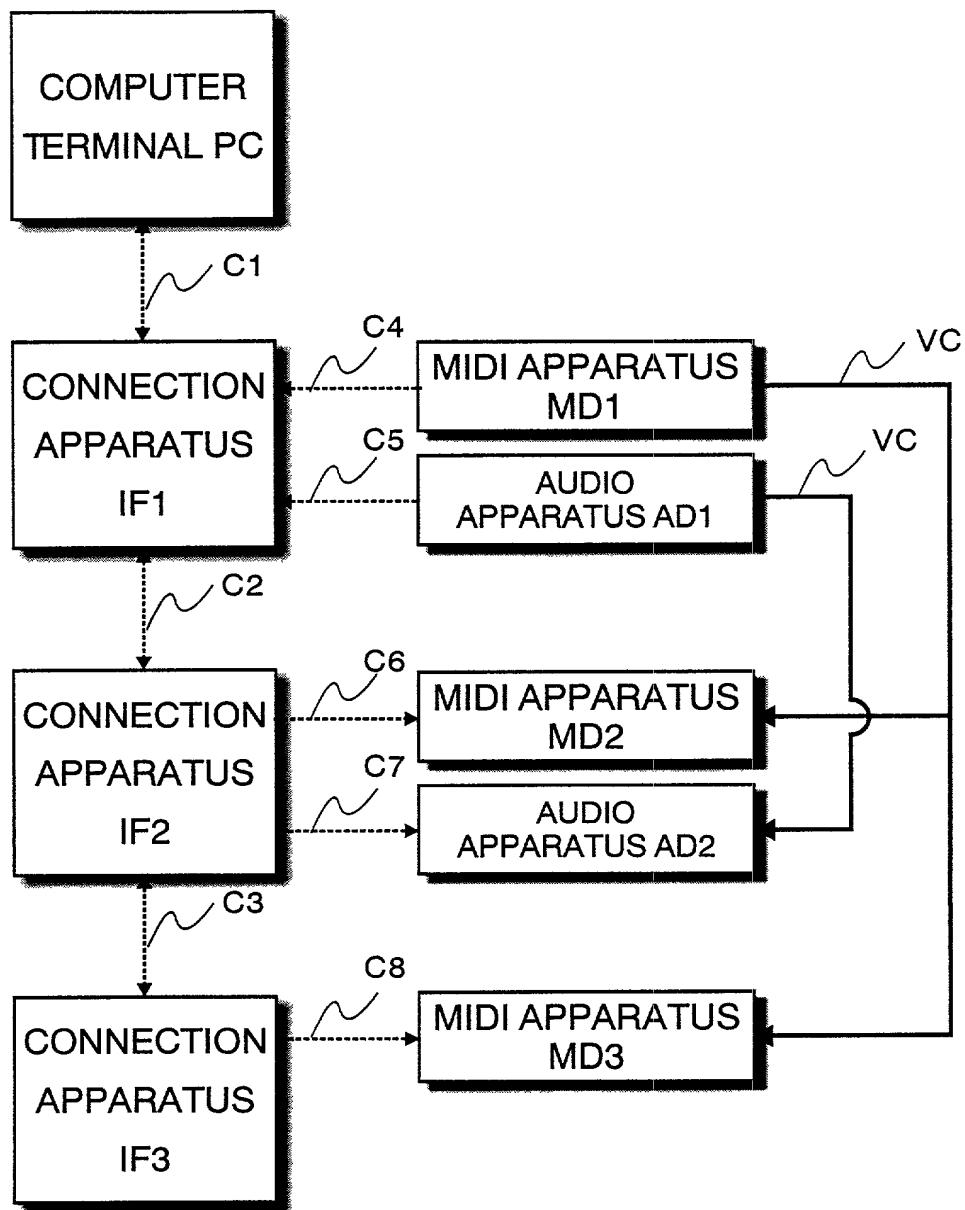
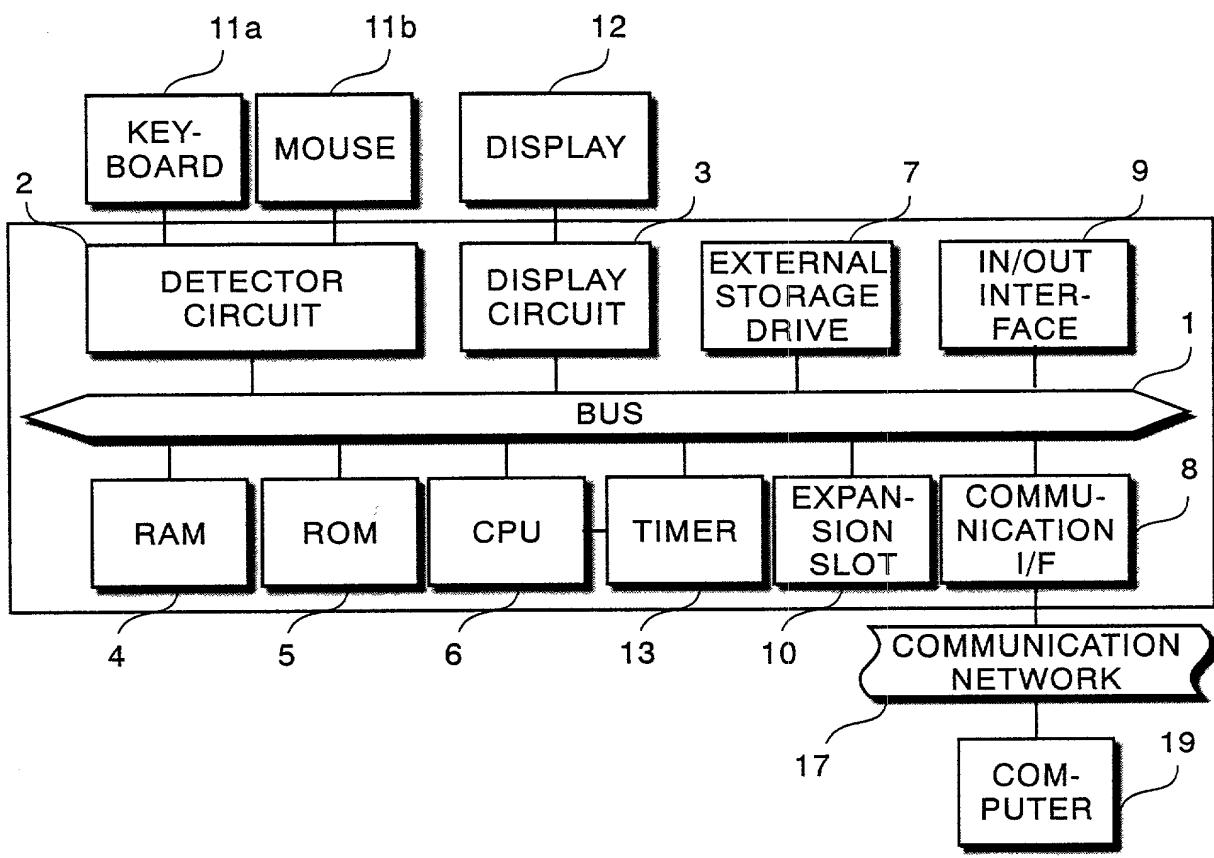


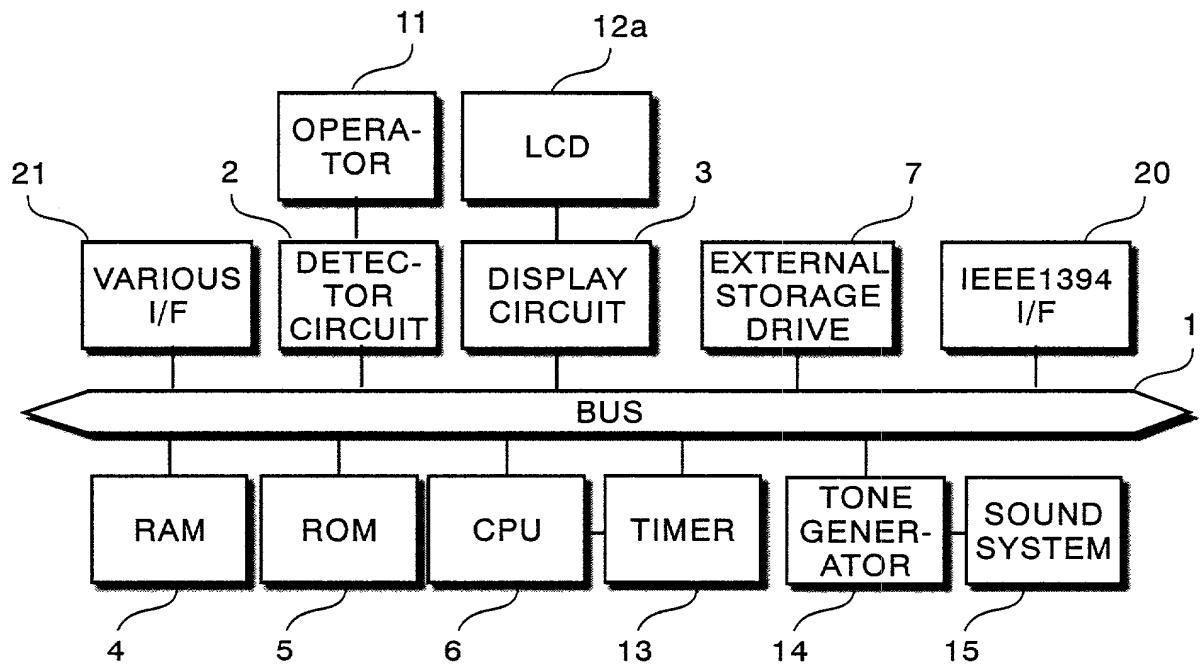
**FIG. 1**



**FIG. 2**



**FIG. 3**



## FIG. 4A

<MIDI>		<AUDIO>	
[from]		[to]	
LINE NAME	APPARATUS NAME	LINE NAME	APPARATUS NAME
MIDlout1	MD1	-----	-----
MIDlout2	-----	-----	-----
MIDlout3	-----	-----	-----
		MIDlin1	
		MIDlin2	MD2
		MIDlin3	MD3

## FIG. 4B

<MIDI>		<AUDIO>	
[from]		[to]	
LINE NAME	APPARATUS NAME	LINE NAME	APPARATUS NAME
AUDIOout1	AD1	-----	-----
AUDIOout2	-----	-----	-----
AUDIOout3	-----	-----	-----
		AUDIOin1	
		AUDIOin2	AD2
		AUDIOin3	

## FIG. 5A

<MIDI>		<AUDIO>	
[from]		[to]	
<u>LINE NAME</u>	<u>APPARATUS NAME</u>	<u>LINE NAME</u>	<u>APPARATUS NAME</u>
MIDIout1	MD1	<b>MIDIlin2</b>	<b>MD2</b>
		<b>MIDIlin3</b>	<b>MD3</b>
MIDIout2	-----	-----	-----
MIDIout3	-----	-----	-----

	MIDIlin1	
	MIDIlin2	MD2
	MIDIlin3	MD3

## FIG. 5B

<MIDI>		<AUDIO>	
[from]		[to]	
<u>LINE NAME</u>	<u>APPARATUS NAME</u>	<u>LINE NAME</u>	<u>APPARATUS NAME</u>
AUDIOout1	AD1	<b>AUDIOin2</b>	<b>AD2</b>
		-----	-----
AUDIOout2	-----	-----	-----
AUDIOout3	-----	-----	-----

	AUDIOin1	-----
	AUDIOin2	AD2
	AUDIOin3	

**FIG. 6A**

SERIAL PORT	IF1
OTHER TERMINALS	

**FIG. 6B**

SERIAL PORT	PC
IEEE1394(mLAN)1	IF2
IEEE1394(mLAN)2	
MIDIin	MD1
MIDIout	
AUDIOin	AD1
AUDIOout	

**FIG. 6C**

SERIAL PORT	
IEEE1394(mLAN)1	IF1
IEEE1394(mLAN)2	IF3
MIDIin	
MIDIout	MD2
AUDIOin	
AUDIOout	AD2

**FIG. 6D**

SERIAL PORT	
IEEE1394(mLAN)1	
IEEE1394(mLAN)2	IF2
MIDIin	
MIDIout	MD3
AUDIOin	
AUDIOout	

**FIG. 7A**

MIDIin1	
MIDIout1	MIDIin
AUDIOin1	
AUDIOout1	AUDIOin

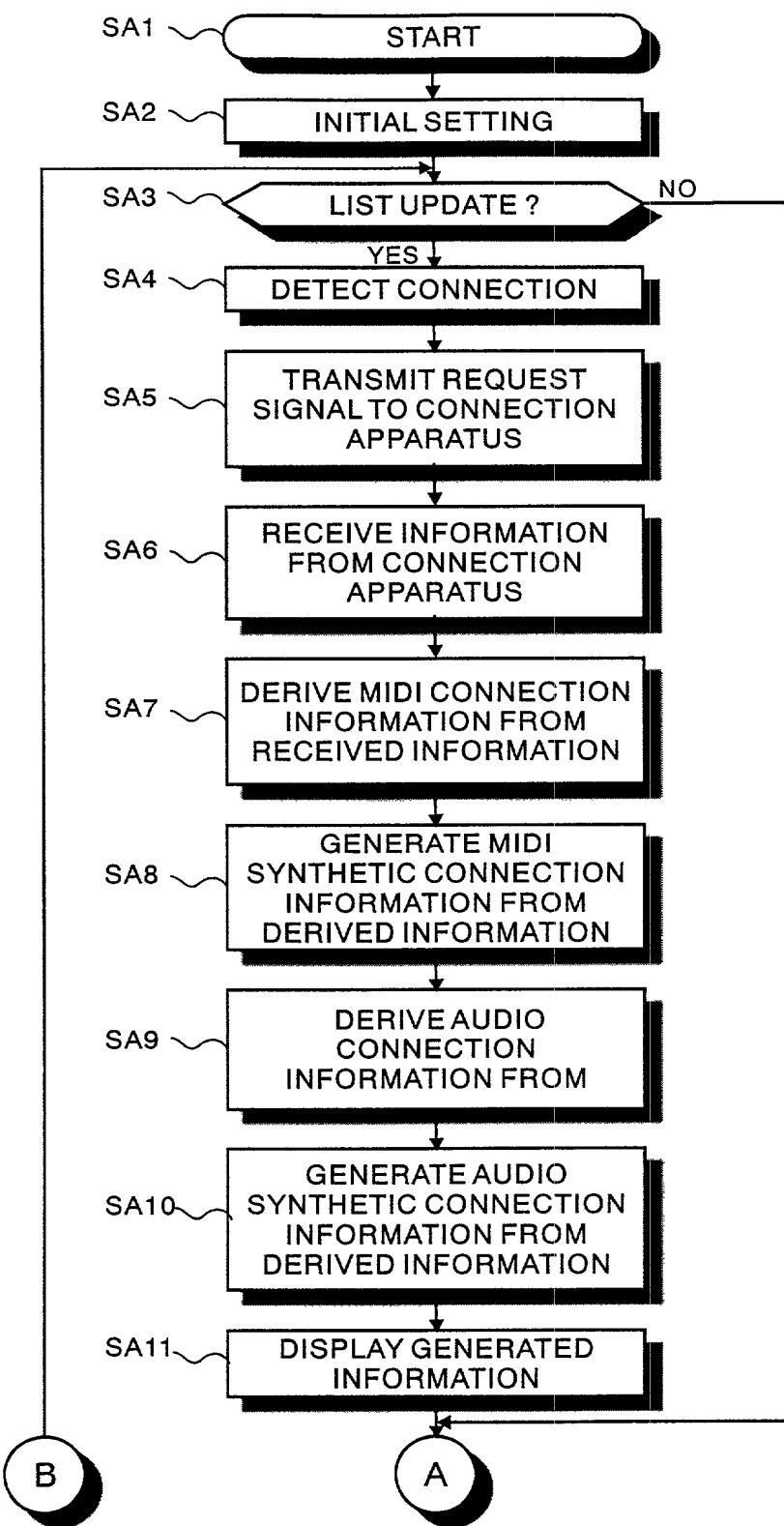
**FIG. 7B**

MIDIin2	
MIDIout2	
AUDIOin2	
AUDIOout2	AUDIOout

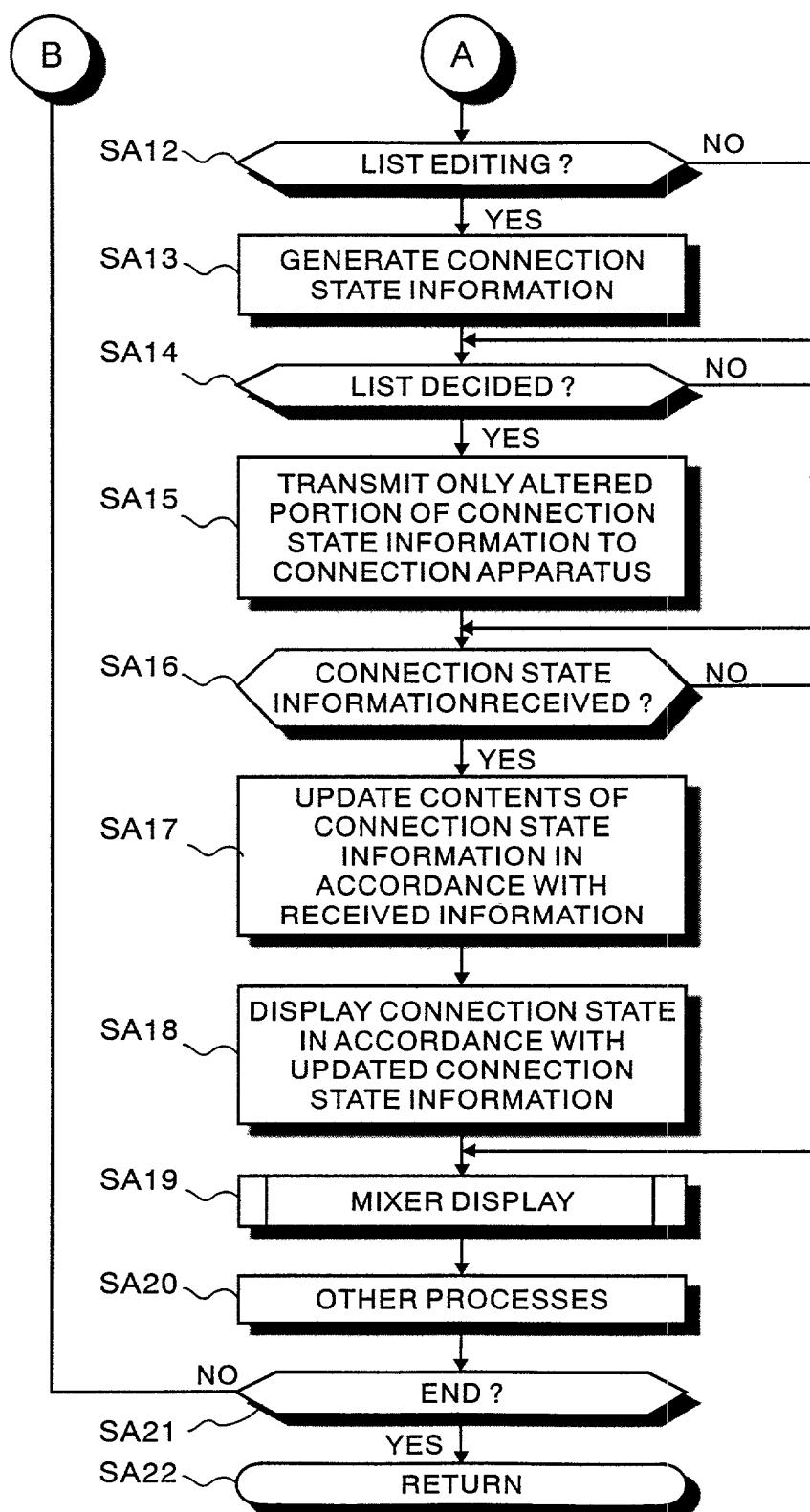
**FIG. 7C**

MIDIin3	MIDIout
MIDIout3	
AUDIOin3	
AUDIOout3	

**FIG. 8**



**FIG. 9**



**FIG. 10A**

LINE NAME	APPARATUS NAME
MIDIin1	
MIDIin2	MD2
MIDIin3	MD3
MIDIout1	MD1
MIDIout2	
MIDIout3	

**FIG. 10B**

LINE NAME	APPARATUS NAME
AUDIOin1	
AUDIOin2	AD2
AUDIOin3	
AUDIOout1	AD1
AUDIOout2	
AUDIOout3	

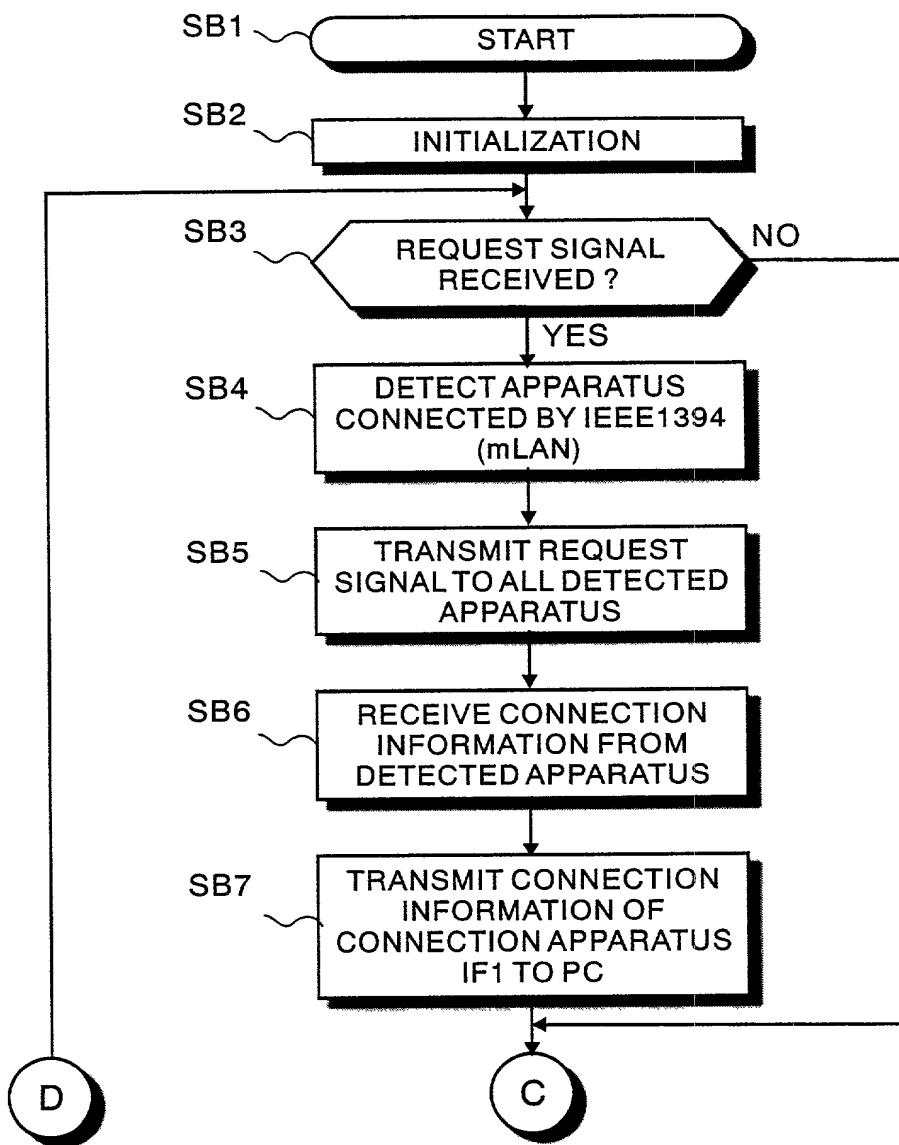
**FIG. 11A**

from	to
MIDIout1	MIDIin2    MIDIin3
MIDIout2	
MIDIout3	

**FIG. 11B**

from	to
AUDIOout1	AUDIOin2
AUDIOout2	
AUDIOout3	

**FIG. 12**



**FIG. 13**

